

**DR. BABASAHEB AMBEDKAR TECHNOLOGICAL  
UNIVERSITY, LONERE - RAIGAD -402 103  
Winter Semester Examination - December - 2019**

**Branch: Electrical Engineering**

**Subject :- Microprocessor & Microcontroller (BTEEC503)**

**Date:- 13/12/2019**

**Sem.:- V**

**Marks: 60**

**Time:- 3 Hr.**

**Instructions to the Students**

1. Each question carries 12 marks.
2. Attempt **any five** questions of the following.
3. Illustrate your answers with neat sketches, diagram etc., wherever necessary.
4. If some part or parameter is noticed to be missing, you may appropriately assume it and should mention it clearly

		(Marks)
<b>Q.1.</b>	a) Draw pin configuration of $\mu\text{p}$ 8085. Explain functions of following pins: i) X1, X2 ii) ALE iii) S <sub>0</sub> , S <sub>1</sub>	<b>06</b>
	b) How is a SIM instruction used to set interrupts? Illustrate with an example.	<b>06</b>
<b>Q.2.</b>	a) Explain the need of de-multiplexing of AD <sub>0</sub> -AD <sub>7</sub> in the memory interfacing with $\mu\text{p}$ 8085. Also explain any one memory mapping scheme.	<b>06</b>
	b) Interface 4k RAM with $\mu\text{p}$ 8085 using 1k $\times$ 8 RAM.	<b>06</b>
<b>Q.3.</b>	a) What are the types of interrupts in $\mu\text{p}$ 8085? Explain all software interrupts with their vector locations and priorities.	<b>06</b>
	b) What is DMA? Draw and explain pin configuration of DMA IC 8237 in details.	<b>06</b>
<b>Q.4.</b>	a) What are the types of digital to analog converter? Draw pin configuration of DAC 0808.	<b>06</b>
	b) Write an assembly language program to generate square waveform on CRO using an interfacing IC 8255 with $\mu\text{p}$ 8085.	<b>06</b>
<b>Q.5.</b>	a) Draw the functional block diagram of micro-controller 8051 and explain each block in detail.	<b>06</b>
	b) What is program status word register in $\mu\text{c}$ 8051? Draw structure of program status word and explain each flag.	<b>06</b>
<b>Q.6.</b>	a) Explain following instructions of $\mu\text{c}$ 8051: i) MOV A,#55H ii) ADD A,R2 iii) ORG 0H	<b>06</b>
	b) Draw and explain DC motor controller using a Darlington Transistor.	<b>06</b>

\*\*\*\*\* Paper End \*\*\*\*\*